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JAPANESE PATENT OFFICE

PATENT ABSTRACTS OF JAPAN

(11) Publication number: 07287669

(43) Date of publication of application: 31.10.1995

(51)Int.Cl.

G06F 12/08

(21) Application number: 06078370

(71)Applicant:

HITACHI LTD

(22) Date of filing: 18.04.1994

(72) Inventor:

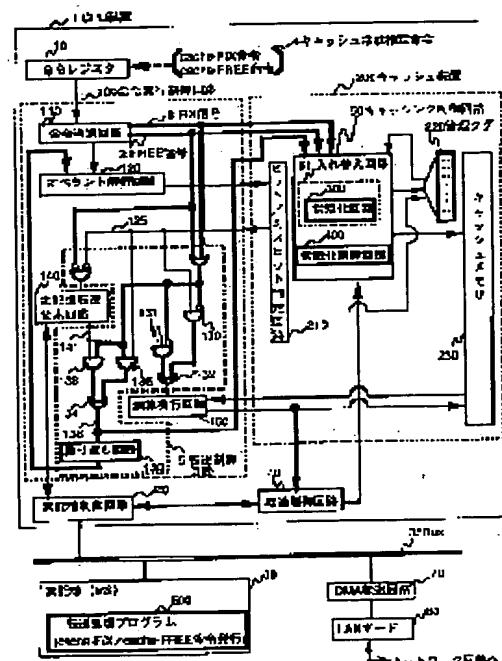
TAKAHASHI KIKUO

(54) DATA PROCESSOR AND PROGRAM EXECUTING METHOD USING THE SAME

(57)Abstract:

PURPOSE: To provide a data processor and a fast data-transfer method in order to suppress the deterioration of the CPU processing efficiency caused by a caching operation.

CONSTITUTION: A data processor includes an instruction 4 with which a program can clearly designate the data stored in a main storage and to be shifted into a cache memory, a transfer control circuit 5 which transfers the data to the cache memory from the main memory, a circuit 300 which makes the transferred data reside in the cache memory, and a circuit 400 which invalidates the residence of the data in the cache memory. Then a specific program 500 to be processed with preference controls a caching operation in accordance with the characteristic of the corresponding processing procedure.



LEGAL STATUS